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Substitute for form 1448/PTO

**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

(Use as many sheets as necessary)

Complete if Known

Application Number	TBA 10619648
Filing Date	July 14, 2003
First Named Inventor	Steven R. HETZLER
Art Unit	TBA
Examiner Name	TBA
Attorney Docket Number	ARC9-2003-0040-US1

Sheet 1 of 4

U. S. PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code ² (if known)			
lee	1	US- 5,134,619	07-28-1992	Henson et al.	
lee	2	US- 5,148,432	09-15-1992	Gordon et al.	
lee	3	US- 5,257,391	10-26-1993	DuLac et al.	
lee	4	US- 5,301,297	04-05-1994	Menon et al.	
lee	5	US- 5,398,253	03-14-1995	Gordon	
lee	6	US- 5,506,977	04-09-1996	Jones	
lee	7	US- 5,579,475	11-26-1996	Blaum et al.	
lee	8	US- 5,835,938	11-10-1998	Yamamoto et al.	
lee	9	US- 5,848,229	12-08-1998	Morita	
lee	10	US- 5,937,428	08-10-1999	Jantz	
lee	11	US- 6,070,249	05-30-2000	Lee	
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lee	14	US- 6,161,165	12-12-2000	Solomon et al.	
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lee	18	US- 6,353,895 B1	03-05-2002	Stephenson	
lee	19	US- 6,530,004 B1	03-04-2003	King et al.	

FOREIGN PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ³
		Country Code ⁴ Number ⁴ Kind Code ⁵ (if known)				

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

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		Examiner Name	TBA		
Sheet	3	of	4	Attorney Docket Number	ARC9-2003-0040-US1

NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
lll	21	G.A. ALVAREZ et al., Tolerating Multiple Failures in RAID Architectures with Optimal Storage and Uniform Decustering, Computer Architecture News (USA), V. 25, #2, pp. 62-72, May 1972.	
lll	22	V. BOHOSSIAN et al., Computing in the RAIN: A Reliable Array of Independent Nodes, pp. 1-20, September 24, 1999.	
lll	23	P.M. CHEN et al., RAID: High-Performance, Reliable Secondary Storage, ACM Computing Surveys, Vol. 26, No. 2, pp. 146-185, June 1994.	
lll	24	M. HOLLAND et al., Parity Decustering for Continuous Operation in Redundant Disk Arrays, ACM 0-89791-535-6/92/0010/0023, pp. 23-35, October 1992.	
lll	25	N.K. OUCHI, Two-Level DASD Failure Recovery Method, IBM Technical Disclosure Bulletin, Vol. 36, No. 03, pp. 187-190, March 1993.	
lll	26	D.A. Patterson et al., A Case for Redundant Arrays of Inexpensive Disks (RAID), ACM 0-89791-268-3/88/0006/0109 1998.	
lll	27	J.S. PLANK, A Tutorial on Reed-Solomon Coding for Fault-Tolerance in RAID-like Systems, pp. 1-19, February 19, 1999.	
lll	28	E.J. SCHWABE et al., Evaluating Approximately Balanced Parity-Declassified Data Layouts for Disk Arrays, ACM 0-89791-813-4/96/05, pp. 41-54, 1996.	
lll	29	E.J. SCHWABE et al., Flexible Usage of Parity Storage Space in Disk Arrays, ACM 0-89791-809-6/96/06, pp. 99-108, 1996.	
lll	30	L. Xu et al., X-Code: MDS Array Codes with Optimal Encoding, IEEE Trans. On Information Theory, Vol. 45, No. 1, pp. 272-276, January 1999.	

Examiner Signature		Date Considered	5/19/06
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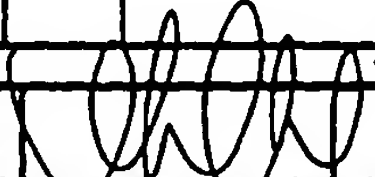
¹ Applicant's unique citation designation number (optional). ² Applicant is to place a check mark here if English language Translation is attached.

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Sheet	4	of	4	Attorney Docket Number	ARC9-2003-0040-US1

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lll	31	M. Blaum et al., "MDS Array Codes with Independent Parity Symbols," IEEE Trans. on Information Theory, Vol. IT-42, pp. 529-542, March 1986.	
lll	32	M. Blaum et al., "The EVENODD Code and its Generalization," High Performance Mass Storage and Parallel I/O: Technologies and Applications, edited by H. Jin et al., IEEE & Wiley Press, New York, Chapter 14, pp. 187-208, 2001.	
lll	33	M. Blaum et al., "EVENODD: An Efficient Scheme For Tolerating Double Disk Failures In A RAID Architecture," IEEE Trans. on Computers, Vol. 44, No. 2, pp. 192-202, Feb. 1995.	
Examiner Signature			Date Considered 5/19/06

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